10,085009 10,085009

U.S. UTILITY Patent Application

PATENT NUMBER and ISSUE DATE

APPLICANTS: Fukui Masahiro; Hayashi Naoki; 2818 CONTINUING DATA VERIFIED: NASE FOREIGN APPLICATIONS VERIFIED: VES PAN 2001-063491 03/07/2001 SPUB DO NOT PUBLISH RESCIND ATTORNEY DOCKET NO USC 119 conditions met Ves Inc. 12 no South Continuing method in layout design of serniconductor integrated circuit, semiconductor integrated uit and functional macro US. DEPT. CF. COMM.PATA TM7TO-ASSLIRE. 12-261	APPL NUM FILING I	1	1 . 1	GAU	A	EXAMINER	-
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NOTICE OF ALLOWANCE MAILED			CLAIMS ALLOWED.					
ISSUE FEE		Assistant Examiner	Total Claims	-	Print Claim for O.G			
			DRAWING					
Amount Due	Date Paid		Shoots Drag.	Figs.Di	ræg: Print Fig.			
		Primary Examiner						
TER	RMINAL	PREPARED FOR ISSUE	Applicati n	Examir	ler ·			
	DISCLAMER	Unauthorized disclosure may be prohibi Sections 122, 181 and 368, Possession of	ARNING: The information disclosed herein may be restricted. authorized disclosure may be prohibited by the United States Code Title 35, tions 122, 181 and 368, Possession outside the U.S. Patent & Trademark ice is restricted to authorized employees and contractors only.					
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